

## ABSTRACT:

The invention relates to a communications network (1) comprising a plurality of network nodes (2), which include each a synchronization circuit (5) for generating a global clock signal (GT) from a local clock signal (LT) formed by a clock generator (4) in dependence on a time of reception of a message. The synchronization circuit (5) includes a  
5 divider arrangement (8) for dividing the local clock signal (LT) in dependence on a correction term (KT) and at least one divider factor which is produced by a scaler arrangement (9). The comparator circuit (10) is provided for forming the correction term by comparing the instant of reception of a message and of the local clock signal LT. Furthermore, the synchronizing circuit (5) includes a divider control (7) which may perform a  
10 change of at least one divider factor when the correction term (KT) exceeds a predefined first threshold.

Fig. 3